

What is claimed is:

1. A display panel driving method for driving a display panel formed with a plurality of pixel cells serving as pixels on each of n display lines every plural sub-fields which make up each field of an input video signal for providing a gradation display,

wherein:

each of said sub-field includes:

an addressing stage for scanning each of pixel cells formed on each of said n display lines from one display line to another to set said pixel cells into either a light emission mode or a light extinction mode based on the input video signal, and a sustain stage for forcing only said pixel cells set in the light emission mode to emit light for a duration corresponding to said sub-field; and

each of said n display lines is scanned in said addressing stage in each of at least two sub-fields in each of said sub-fields in an order different from a scanning order in said addressing stage of each of the other sub-fields.

2. A display panel driving method according to claim 1, wherein each of said n display lines is sequentially scanned one by one in said addressing stage in each of said at least two sub-fields in each of said sub-fields, while each of said n display line is scanned at intervals of k

display lines (k is a natural number) in said addressing stage in each of the other sub-fields.

3. A display panel driving method for driving a display panel formed with a plurality of pixel cells serving as pixels on each of n display lines in accordance with an input video signal for providing a gradation display, said method comprising the step of:

performing an addressing stage for scanning each of said pixel cells formed on each of said n display lines from one display line to another to set said pixel cell into a light emission mode or a light extinction mode based on the input video signal, and a sustain stage for forcing only said pixel cells set in the light emission mode to emit light for a duration corresponding to said sub-field,

wherein each of said n display lines is scanned in said addressing stage in said field or in a field group comprised of a plurality of said fields in an order different from a scanning order in said addressing stage in other fields or in other field groups.